IN THE SPECIFICATION

Please amend the paragraph beginning on page 2, line 22, as follows:

-- According to the above classification, the conventional technique shown in ~~FIG. 34~~ FIGS. 34A and 34B falls within type (b-1), and the conventional technique shown in ~~FIG. 35~~ FIGS. 35A and 35B falls within type (a). That is, the conventional technique of ~~FIG. 34~~ FIGS. 34A and 34B cannot be realized without the data control circuit 100. The conventional technique of ~~FIG. 35~~ FIGS. 35A and 35B cannot be realized without providing a stop period where the functions of the circuit are not affected even when the operation of the memory element 11c is stopped. Thus, the conventional clock control techniques require the above-described special prerequisite condition, which make clock control of the sequential circuit complicated.